

DM74LS47

BCD to 7-Segment Decoder/Driver with Open-Collector Outputs

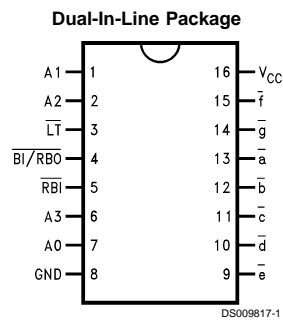
General Description

The 'LS47 accepts four lines of BCD (8421) input data, generates their complements internally and decodes the data with seven AND/OR gates having open-collector outputs to drive indicator segments directly. Each segment output is guaranteed to sink 24 mA in the ON (LOW) state and withstand 15V in the OFF (HIGH) state with a maximum leakage current of 250 μ A. Auxiliary inputs provided blanking, lamp test and cascadable zero-suppression functions.

Features

- Open-collector outputs
- Drive indicator segments directly
- Cascadable zero-suppression capability
- Lamp test input

Connection Diagram



Order Number **DM54LS47J**, **DM54LS47W**,
DM74LS47M or **DM74LS47N**
See Package Number **J16A**, **M16A**, **N16E** or **W16A**

Pin Names	Description
A0–A3	BCD Inputs
$\overline{\text{RBI}}$	Ripple Blanking Input (Active LOW)
$\overline{\text{LT}}$	Lamp Test Input (Active LOW)
$\overline{\text{BI/RBO}}$	Blanking Input (Active LOW) or Ripple Blanking Output (Active LOW)
$\overline{\text{a}}$ – $\overline{\text{g}}$	*Segment Outputs (Active LOW)

Note 1: *OC—Open Collector

Absolute Maximum Ratings (Note 2)

Supply Voltage	7V	DM54LS	-55°C to +125°C
Input Voltage	7V	DM74LS	0°C to +70°C
Operating Free Air Temperature Range		Storage Temperature Range	-65°C to +150°C

Recommended Operating Conditions

Symbol	Parameter	DM54LS47			DM74LS47			Units
		Min	Nom	Max	Min	Nom	Max	
V _{CC}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
V _{IH}	High Level Input Voltage	2			2			V
V _{IL}	Low Level Input Voltage			0.7			0.8	V
I _{OH}	High Level Output Current $\bar{a} - \bar{g}$ @ 15V = V _{OH} (Note 3)			-50			-250	μA
I _{OH}	High Level Output Current $\bar{B}1 / \bar{R}B\bar{O}$						-50	μA
I _{OL}	Low Level Output Current			12			24	mA
T _A	Free Air Operating Temperature	-55		125	0		70	°C

Note 2: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Note 3: OFF state at $\bar{a} - \bar{g}$.

Electrical Characteristics

Over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 4)	Max	Units	
V _I	Input Clamp Voltage	V _{CC} = Min, I _I = -18 mA			-1.5	V	
V _{OH}	High Level Output Voltage	V _{CC} = Min, I _{OH} = Max, V _{IL} = Max, $\bar{B}1 / \bar{R}B\bar{O}$	DM54	2.4		V	
			DM74	2.7	3.4		
I _{OFF}	Output High Current Segment Outputs	V _{CC} = 5.5V, V _O = 15V $\bar{a} - \bar{g}$			250	μA	
V _{OL}	Low Level Output Voltage	V _{CC} = Min, I _{OL} = Max, V _{IH} = Min, $\bar{a} - \bar{g}$	DM54		0.4	V	
			DM74		0.35		0.5
		I _{OL} = 3.2 mA, $\bar{B}1 / \bar{R}B\bar{O}$	DM74				0.5
		I _{OL} = 12 mA, $\bar{a} - \bar{g}$	DM74		0.25		0.4
		I _{OL} = 1.6 mA, $\bar{B}1 / \bar{R}B\bar{O}$	DM74				0.4
I _I	Input Current @ Max Input Voltage	V _{CC} = Max, V _I = 7V	DM74		100	μA	
		V _{CC} = Max, V _I = 10V	DM54				
I _{IH}	High Level Input Current	V _{CC} = Max, V _I = 2.7V			20	μA	
I _{IL}	Low Level Input Current	V _{CC} = Max, V _I = 0.4V			-0.4	mA	
I _{OS}	Short Circuit Output Current	V _{CC} = Max (Note 5), I _{OS} at $\bar{B}1 / \bar{R}B\bar{O}$	DM54	-0.3	-2.0	mA	
			DM74	-0.3	-2.0		
I _{CC}	Supply Current	V _{CC} = Max			13	mA	

Note 4: All typicals are at V_{CC} = 5V, T_A = 25°C.

Note 5: Not more than one output should be shorted at a time, and the duration should not exceed one second.

Switching Characteristics

at $V_{CC} = +5.0V$, $T_A = +25^\circ C$

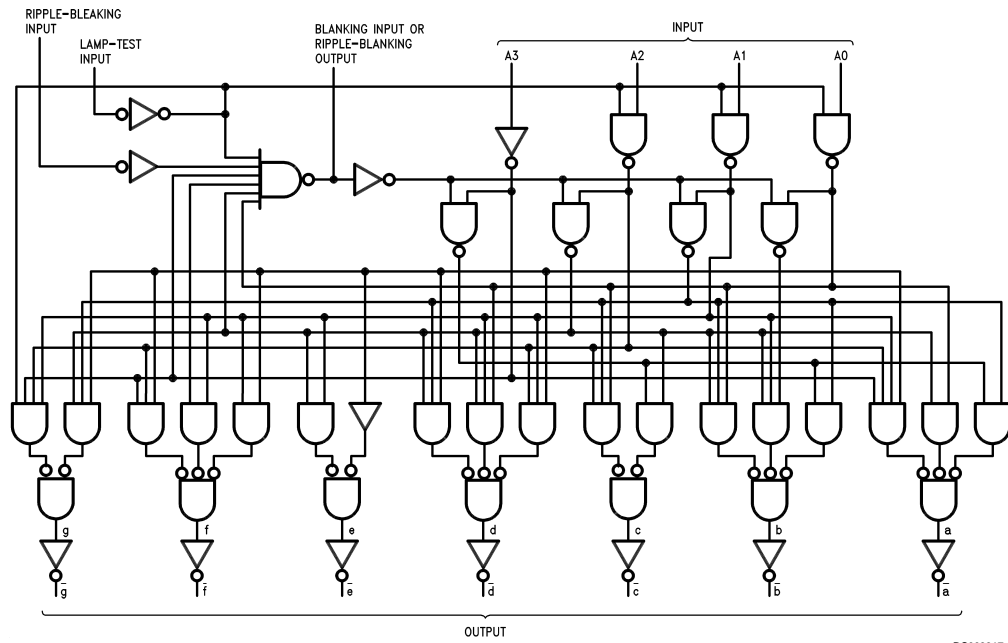
Symbol	Parameter	Conditions	$R_L = 665\Omega$		Units
			$C_L = 15\text{ pF}$		
			Min	Max	
t_{PLH}	Propagation Delay			100	ns
t_{PHL}	An to $\bar{a} - \bar{g}$			100	ns
t_{PLH}	Propagation Delay			100	ns
t_{PHL}	\bar{RBI} to $\bar{a} - \bar{g}$ (Note 6)			100	ns

Note 6: $\bar{LT} = \text{HIGH}$, A0-A3 = LOW

Functional Description

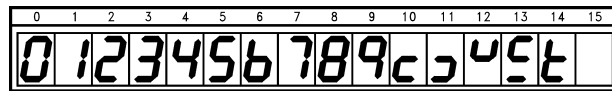
The 'LS47 decodes the input data in the pattern indicated in the Truth Table and the segment identification illustration. If the input data is decimal zero, a LOW signal applied to the \bar{RBI} blanks the display and causes a multidigit display. For example, by grounding the \bar{RBI} of the highest order decoder and connecting its \bar{BI}/\bar{RBO} to \bar{RBI} of the next lowest order decoder, etc., leading zeros will be suppressed. Similarly, by grounding \bar{RBI} of the lowest order decoder and connecting its \bar{BI}/\bar{RBO} to \bar{RBI} of the next highest order decoder, etc., trailing zeros will be suppressed. Leading and trailing zeros can be suppressed simultaneously by using external gates, i.e.: by driving \bar{RBI} of an intermediate decoder from an OR gate whose inputs are \bar{BI}/\bar{RBO} of the next highest and lowest order decoders. \bar{BI}/\bar{RBO} also serves as an unconditional blanking input. The internal NAND gate that generates the \bar{RBO} signal has a resistive pull-up, as opposed to a totem pole, and thus \bar{BI}/\bar{RBO} can be forced LOW by external means, using wired-collector logic. A LOW signal thus applied to \bar{BI}/\bar{RBO} turns off all segment outputs. This blanking feature can be used to control display intensity by varying the duty cycle of the blanking signal. A LOW signal applied to \bar{LT} turns on all segment outputs, provided that \bar{BI}/\bar{RBO} is not forced LOW.

Logic Diagram



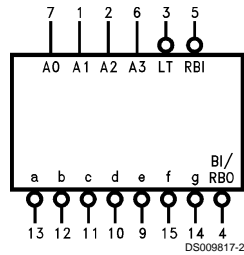
DS009817-3

Numerical Designations—Resultant Displays



DS009817-4

Logic Symbol



V_{CC} = Pin 16
GND = Pin 8

Truth Table

Decimal or Function	Inputs							Outputs							Note											
	LT	RBI	A3	A2	A1	A0	BI/RBO	\bar{a}	\bar{b}	\bar{c}	\bar{d}	\bar{e}	\bar{f}	\bar{g}												
0	H	H	L	L	L	L	H	L	L	L	L	L	L	H	(Note 7)											
1	H	X	L	L	L	H	H	H	L	L	H	H	H	H		(Note 7)										
2	H	X	L	L	H	L	H	L	L	H	L	L	H	L			(Note 7)									
3	H	X	L	L	H	H	H	L	L	L	L	H	H	L				(Note 7)								
4	H	X	L	H	L	L	H	H	L	L	H	H	L	L					(Note 7)							
5	H	X	L	H	L	H	H	L	H	L	L	H	L	L						(Note 7)						
6	H	X	L	H	H	L	H	H	H	L	L	L	L	L							(Note 7)					
7	H	X	L	H	H	H	H	L	L	L	H	H	H	H								(Note 7)				
8	H	X	H	L	L	L	H	L	L	L	L	L	L	L									(Note 7)			
9	H	X	H	L	L	H	H	L	L	L	H	H	L	L										(Note 7)		
10	H	X	H	L	H	L	H	H	H	H	L	L	H	L											(Note 7)	
11	H	X	H	L	H	H	H	H	H	L	L	H	H	L												(Note 7)
12	H	X	H	H	L	L	H	H	L	H	H	H	L	L												
13	H	X	H	H	L	H	H	L	H	H	L	H	L	L	(Note 7)											
14	H	X	H	H	H	L	H	H	H	H	L	L	L	L		(Note 7)										
15	H	X	H	H	H	H	H	H	H	H	H	H	H	H			(Note 7)									
\bar{BI}	X	X	X	X	X	X	L	H	H	H	H	H	H	H				(Note 8)								
\bar{RBI}	H	L	L	L	L	L	L	H	H	H	H	H	H	H				(Note 9)								
\bar{LT}	L	X	X	X	X	X	H	L	L	L	L	L	L	L				(Note 10)								

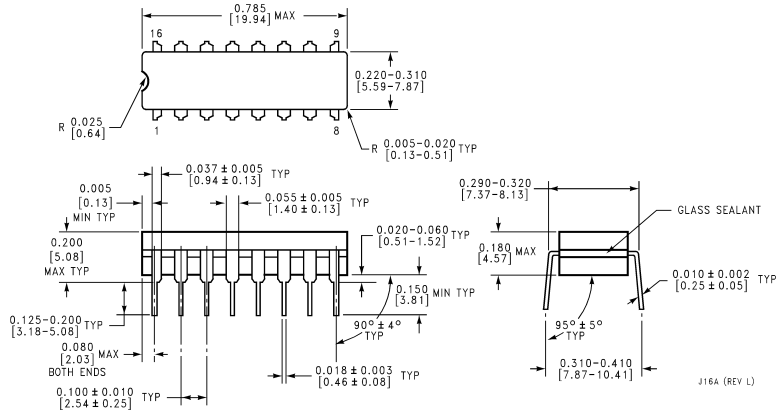
Note 7: BI/RBO is wire-AND logic serving as blanking input (\bar{BI}) and/or ripple-blanking output (RBO). The blanking out (\bar{BI}) must be open or held at a HIGH level when output functions 0 through 15 are desired, and ripple-blanking input (\bar{RBI}) must be open or at a HIGH level if blanking or a decimal 0 is not desired. X = input may be HIGH or LOW.

Note 8: When a LOW level is applied to the blanking input (forced condition) all segment outputs go to a HIGH level regardless of the state of any other input condition.

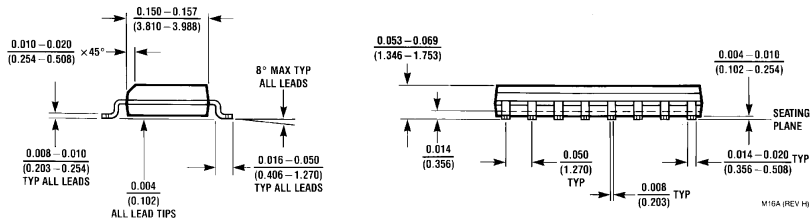
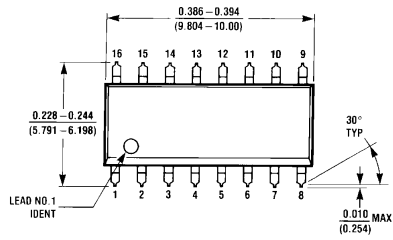
Note 9: When ripple-blanking input (\bar{RBI}) and inputs A0, A1, A2 and A3 are LOW level, with the lamp test input at HIGH level, all segment outputs go to a HIGH level and the ripple-blanking output (RBO) goes to a LOW level (response condition).

Note 10: When the blanking input/ripple-blanking output ($\bar{BI/RBO}$) is open or held at a HIGH level, and a LOW level is applied to lamp test input, all segment outputs go to a LOW level.

Physical Dimensions inches (millimeters) unless otherwise noted

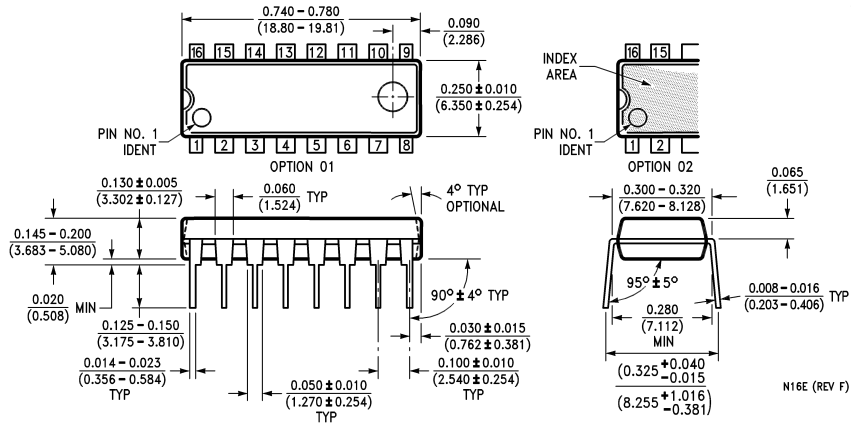


16-Lead Ceramic Dual-In-Line Package (J)
Order Number DM54LS47J
Package Number J16A

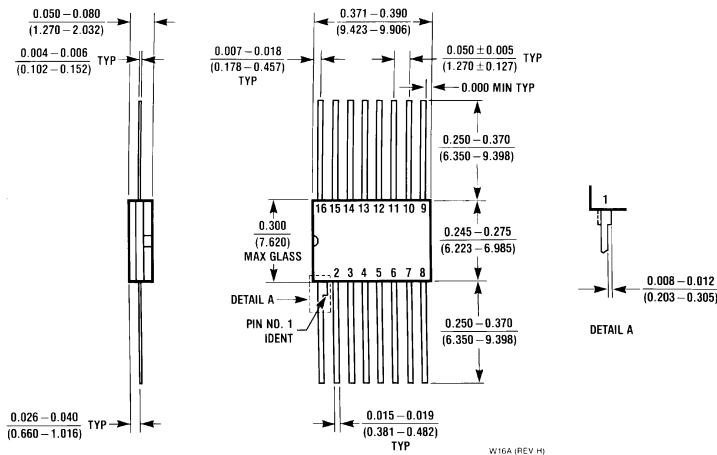


16-Lead Small Outline Molded Package (M)
Order Number DM74LS47M
Package Number M16A

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



16-Lead Molded Dual-In-Line Package (N)
Order Number DM74LS47N
Package Number N16E



16-Lead Ceramic Flat Package (W)
Order Number DM54LS47W
Package Number W16A

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

Fairchild Semiconductor Corporation Americas
Customer Response Center
Tel: 1-888-522-5372

Fairchild Semiconductor Europe
Fax: +49 (0) 1 80-530 85 86
Email: europe.support@nsc.com
Deutsch Tel: +49 (0) 8 141-35-0
English Tel: +44 (0) 1 793-85-68-56
Italy Tel: +39 (0) 2 57 5631

Fairchild Semiconductor Hong Kong Ltd.
13th Floor, Straight Block,
Ocean Centre, 5 Canton Rd.
Tsimshatsui, Kowloon
Hong Kong
Tel: +852 2737-7200
Fax: +852 2314-0061

National Semiconductor Japan Ltd.
Tel: 81-3-5620-6175
Fax: 81-3-5620-6179

www.fairchildsemi.com